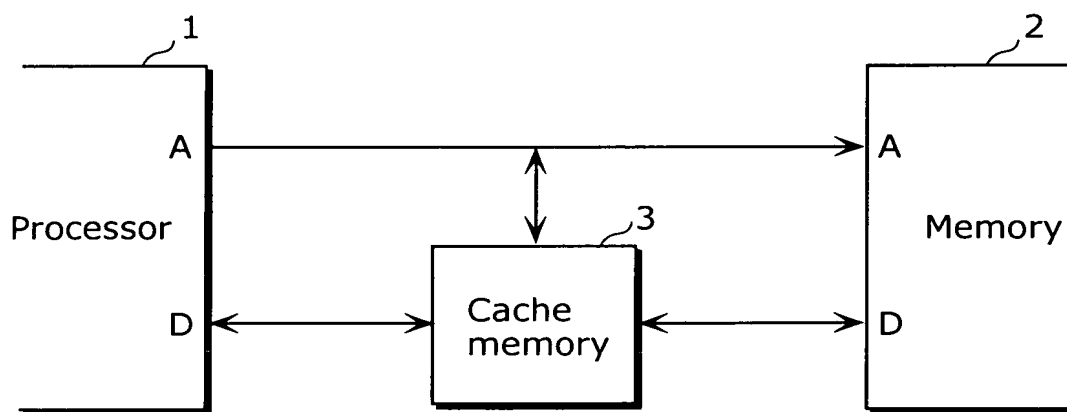


FIG. 1



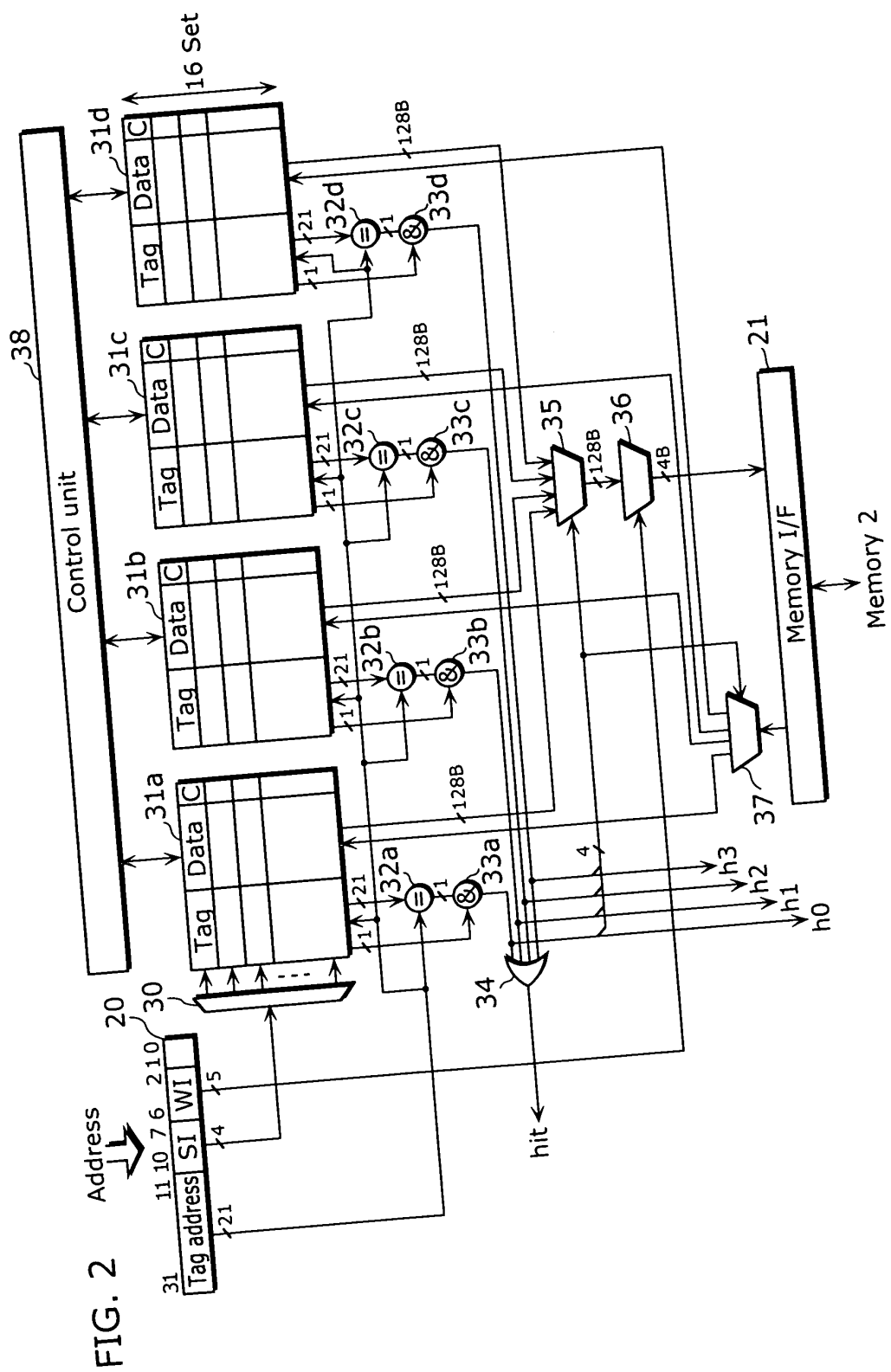


FIG. 3

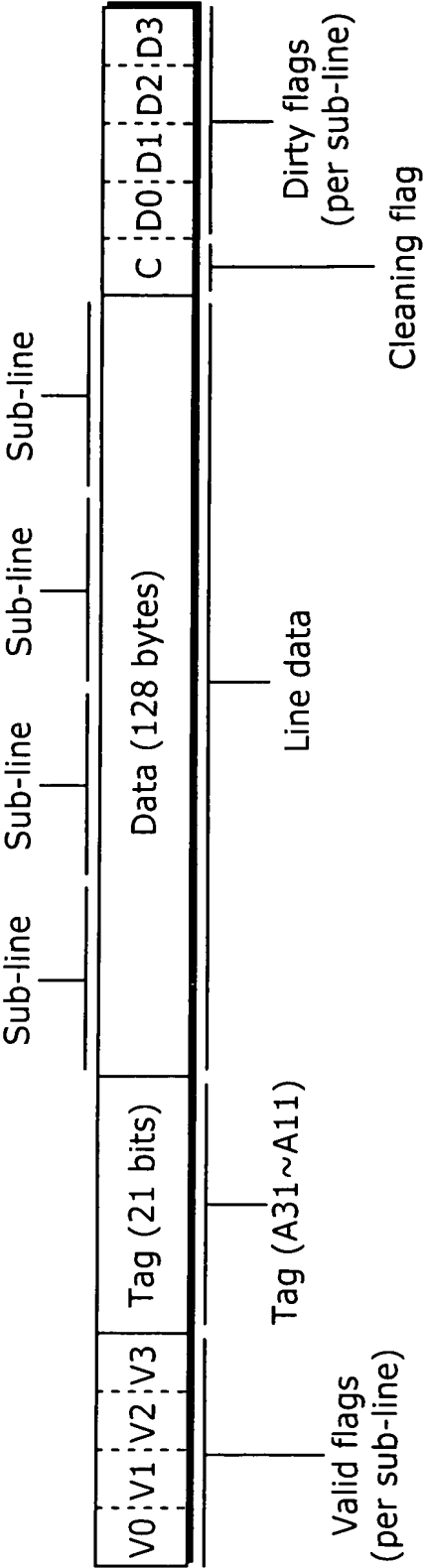


FIG. 4

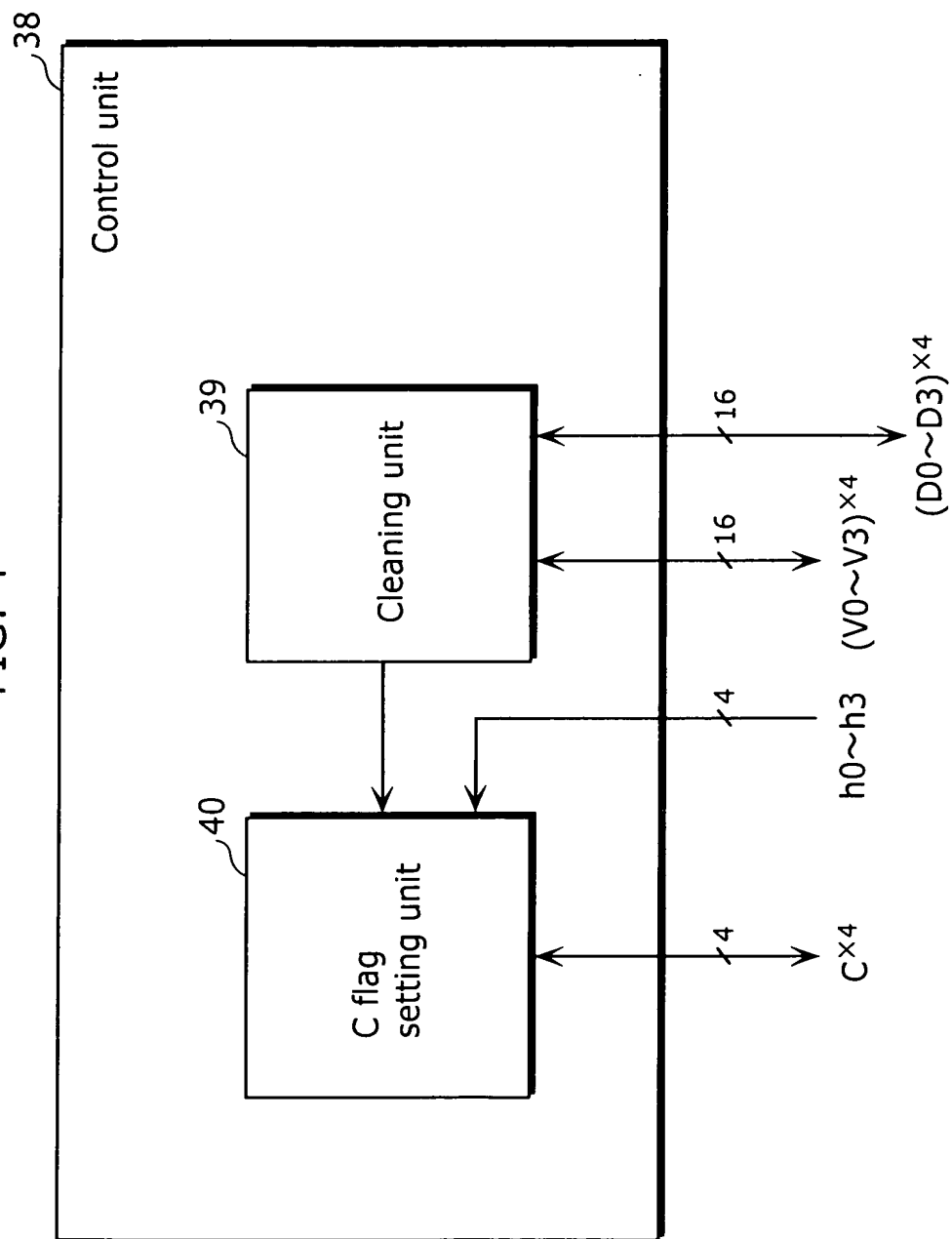
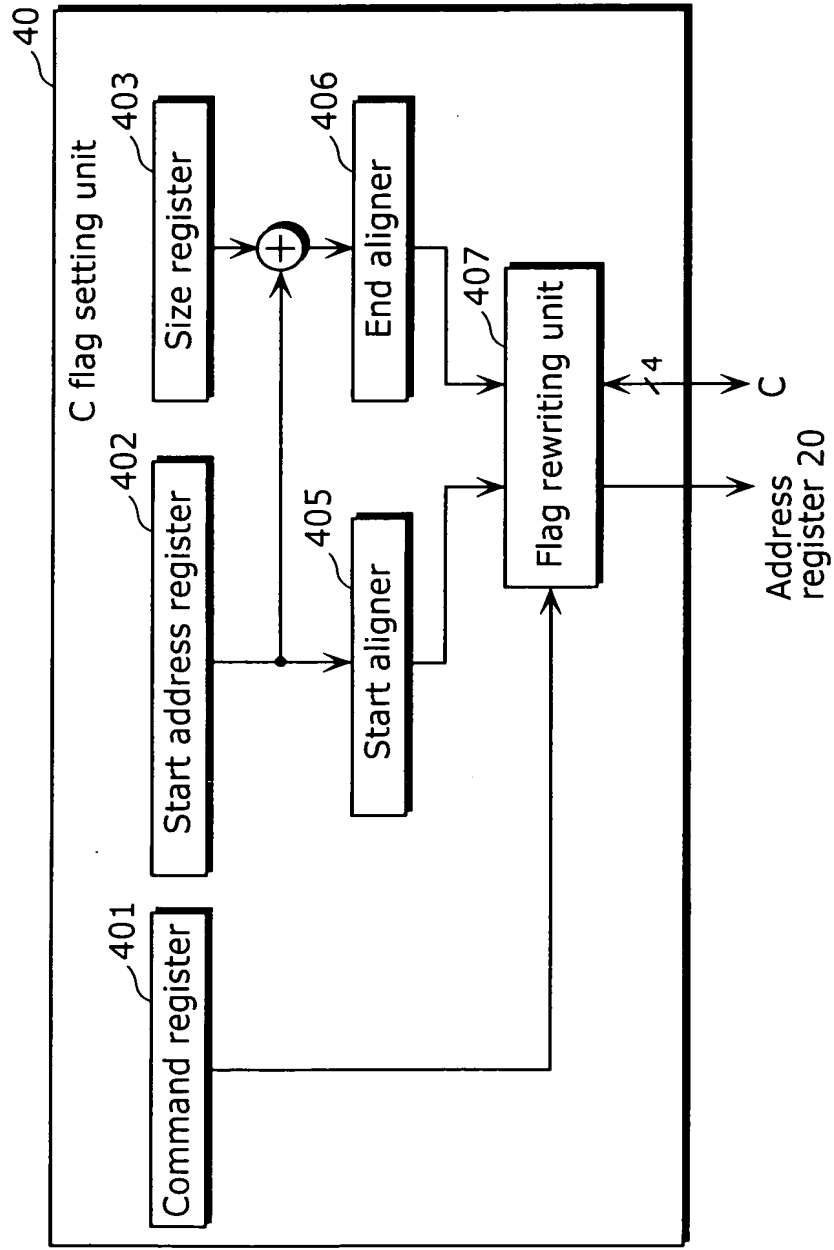


FIG. 5



mov SAR , start_adrs

↑ (Source) specify start address

↑ (Destination) specify start address register

↑ Transfer instruction

Diagram illustrating the instruction format for `mov SR, size`:

- `mov`: Transfer instruction
- `SR`: (Destination) specify size register
- `size`: (Source) specify size

mov CR, command

- (Source) specify command
- (Destination) specify command register
- Transfer instruction

Diagram illustrating the C flag setting command format. The command is a 32-bit register. The first 16 bits (b31 to b16) are reserved (indicated by an 'X'). The remaining 16 bits (b15 to b0) are labeled 'Command'. A bracket below the 'Command' field points to the text 'C flag setting command'.

FIG. 7

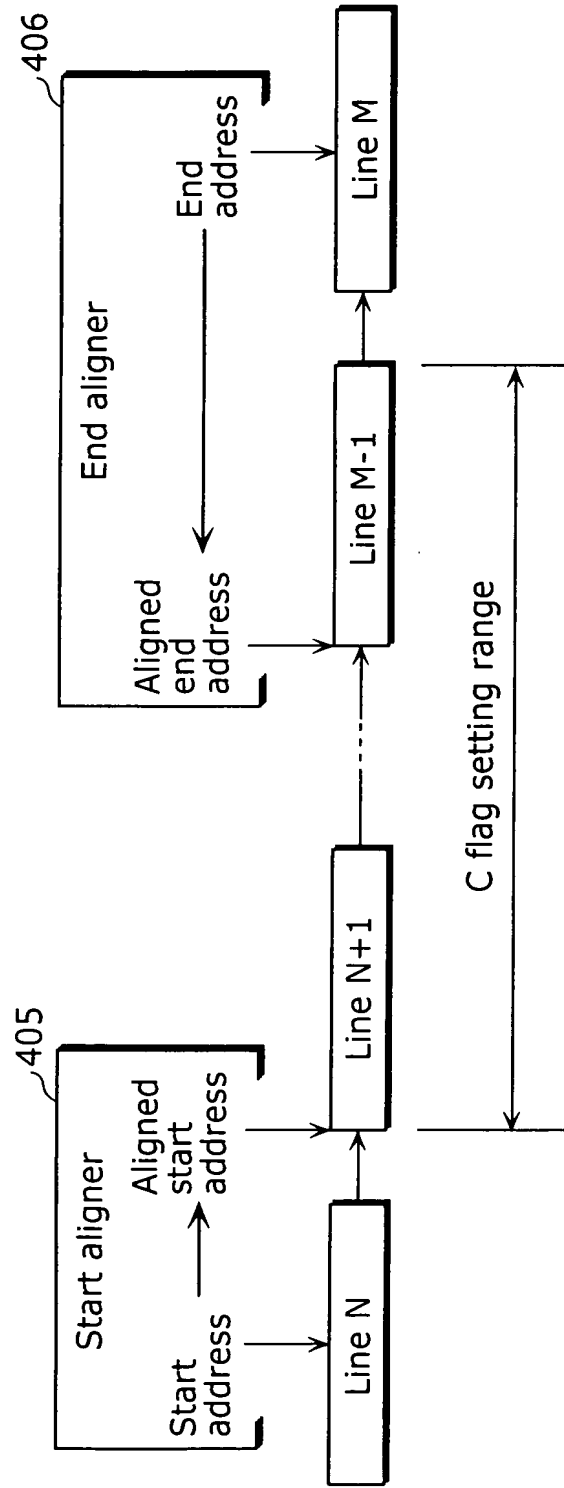


FIG. 8

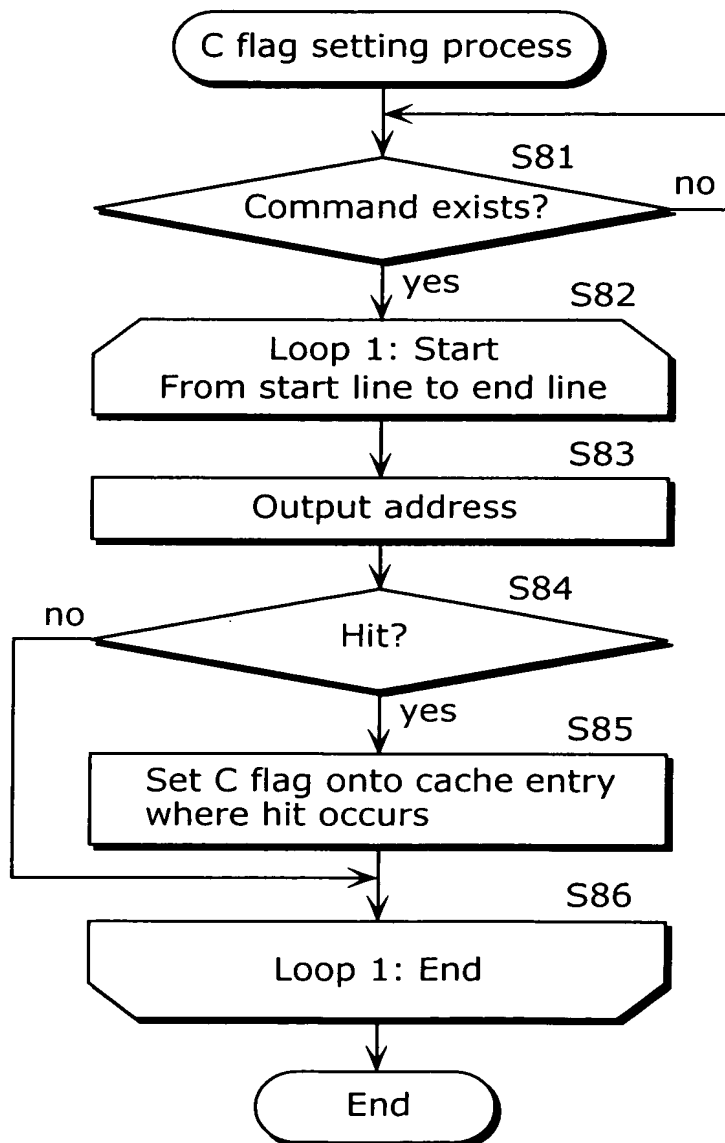


FIG. 9

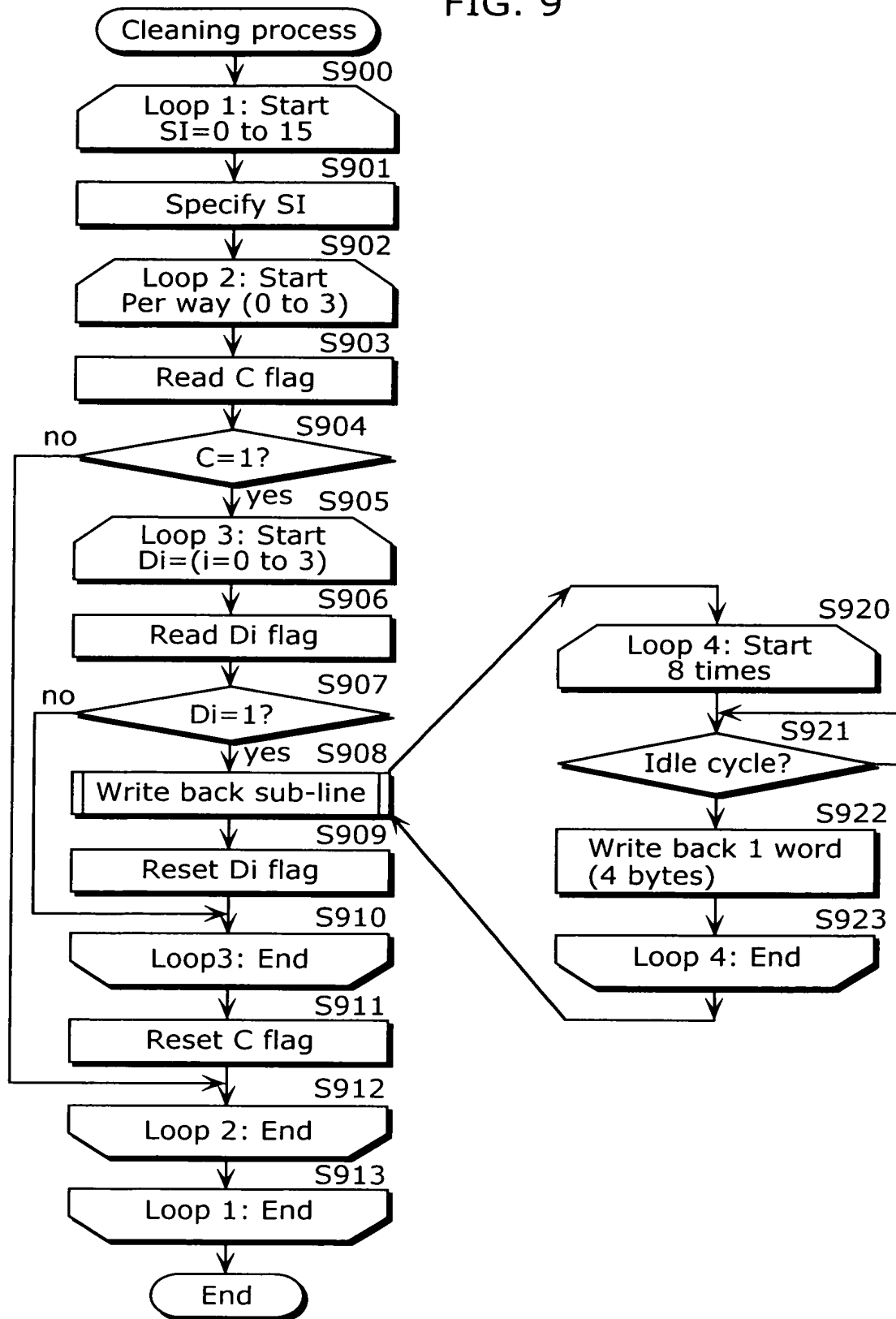


FIG. 11

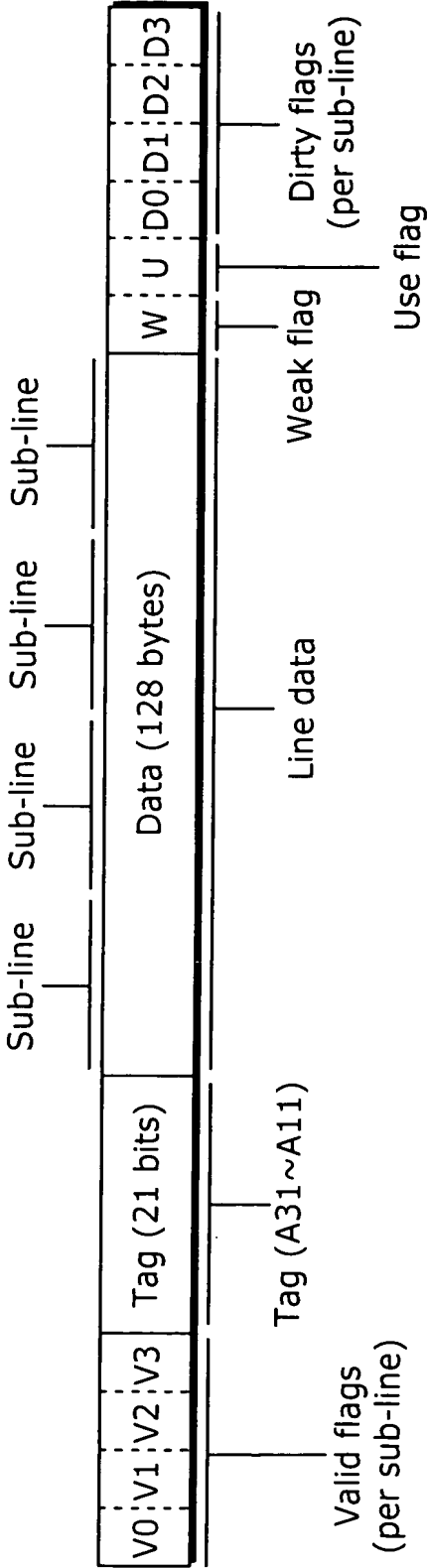


FIG. 12

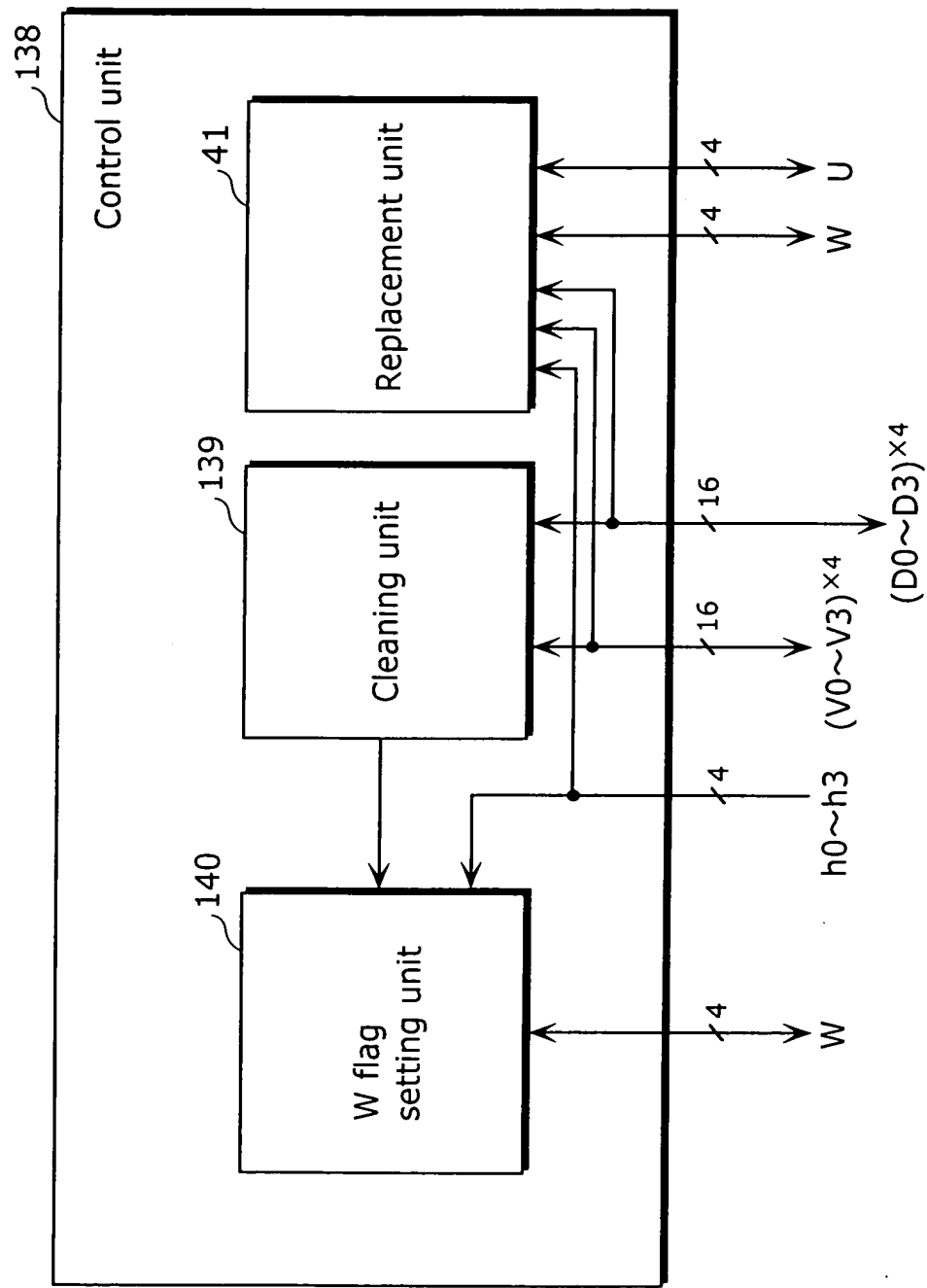


FIG. 13

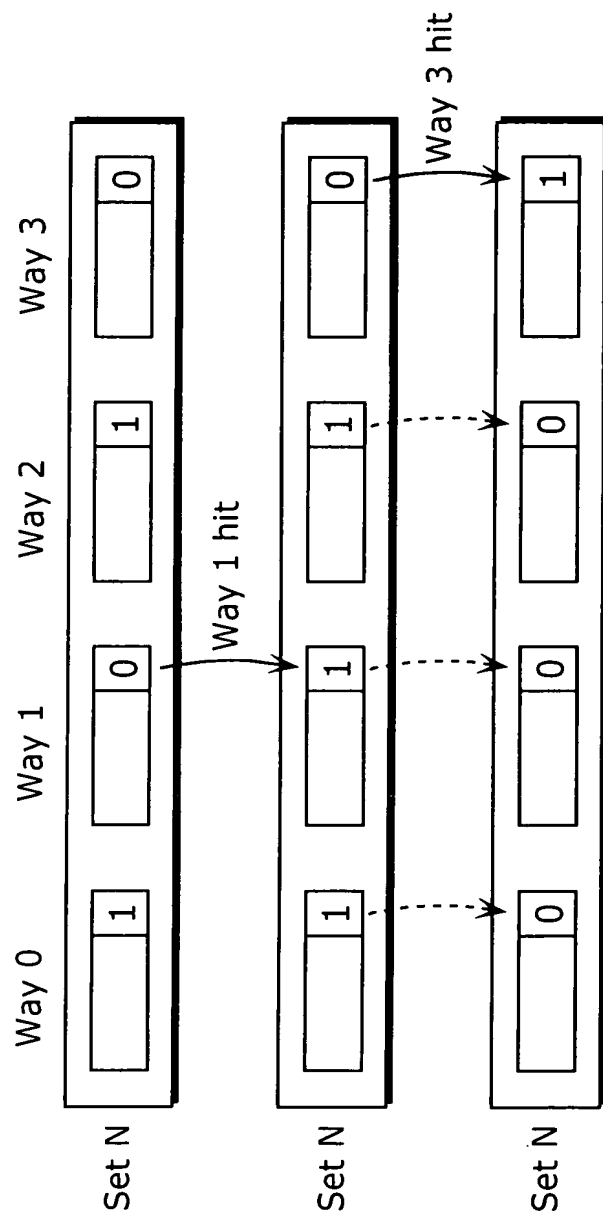


FIG. 14A

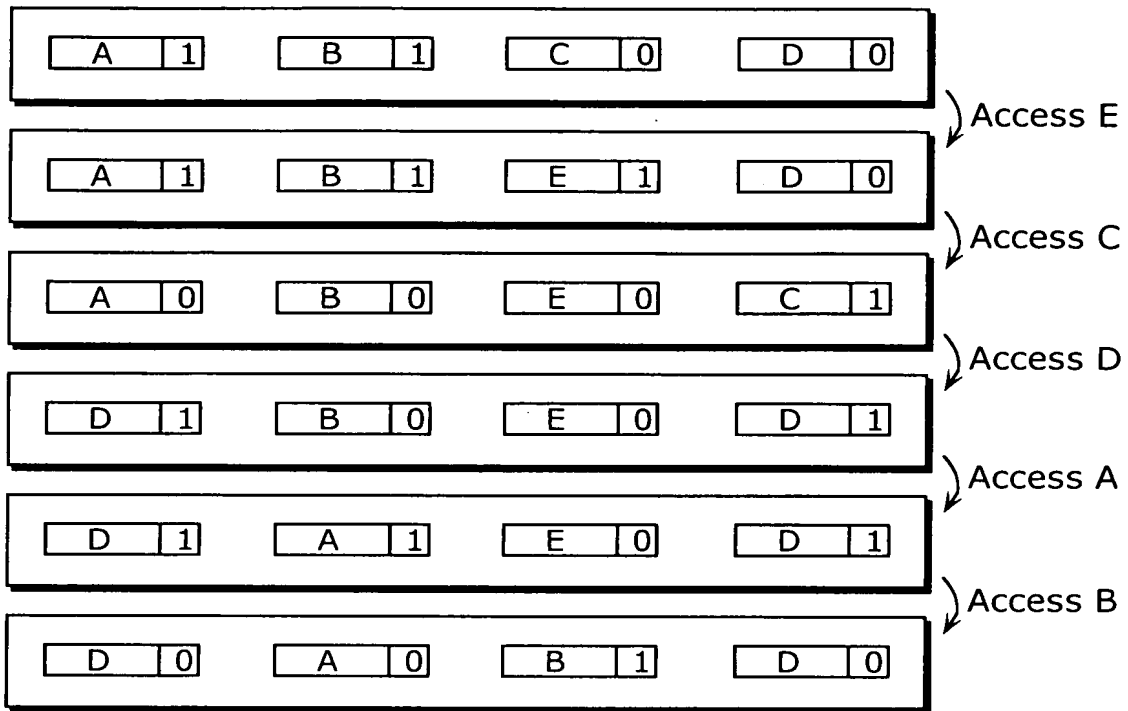


FIG. 14B

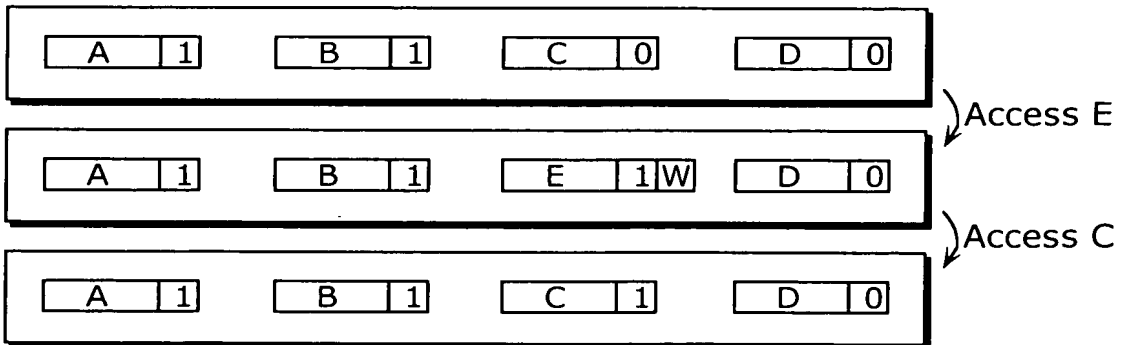


FIG. 15

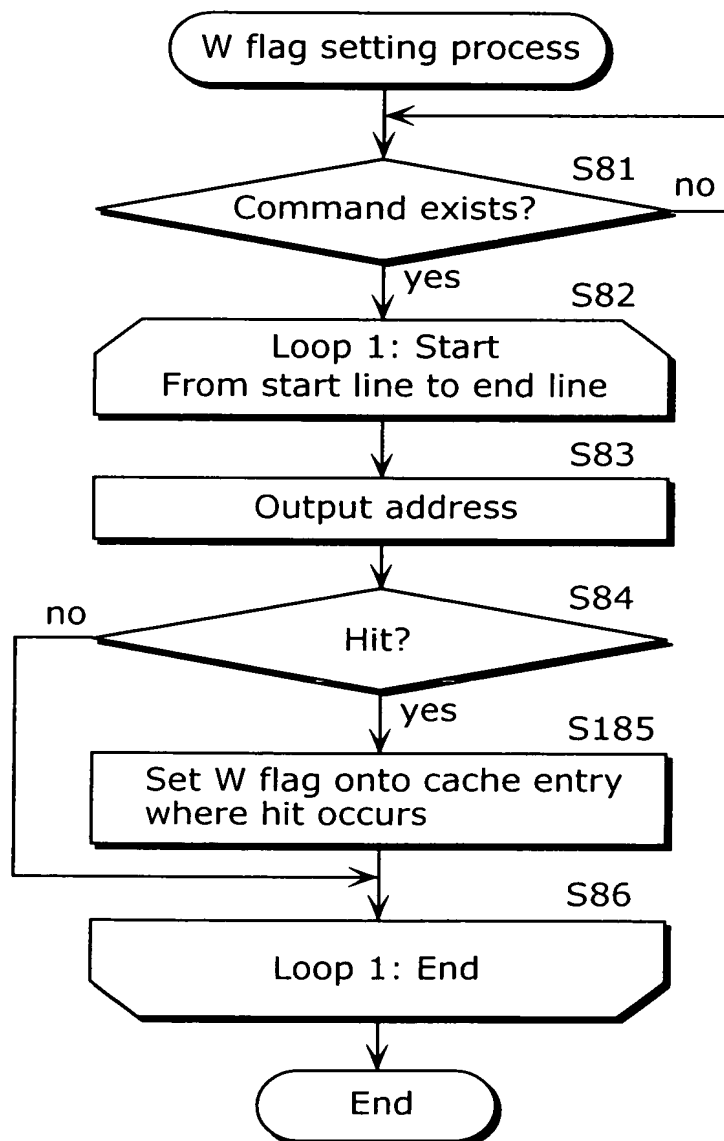


FIG. 16

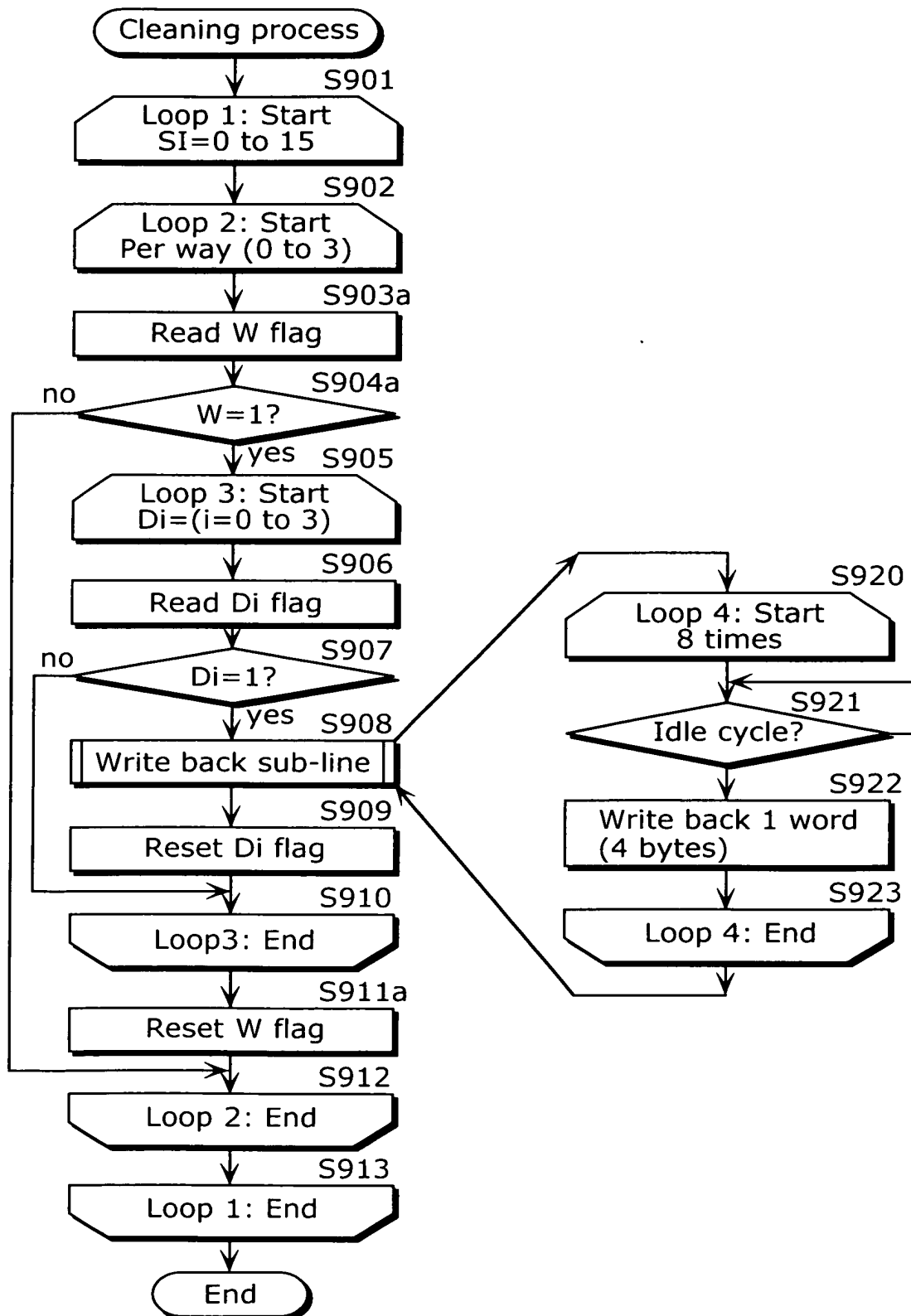


FIG. 17

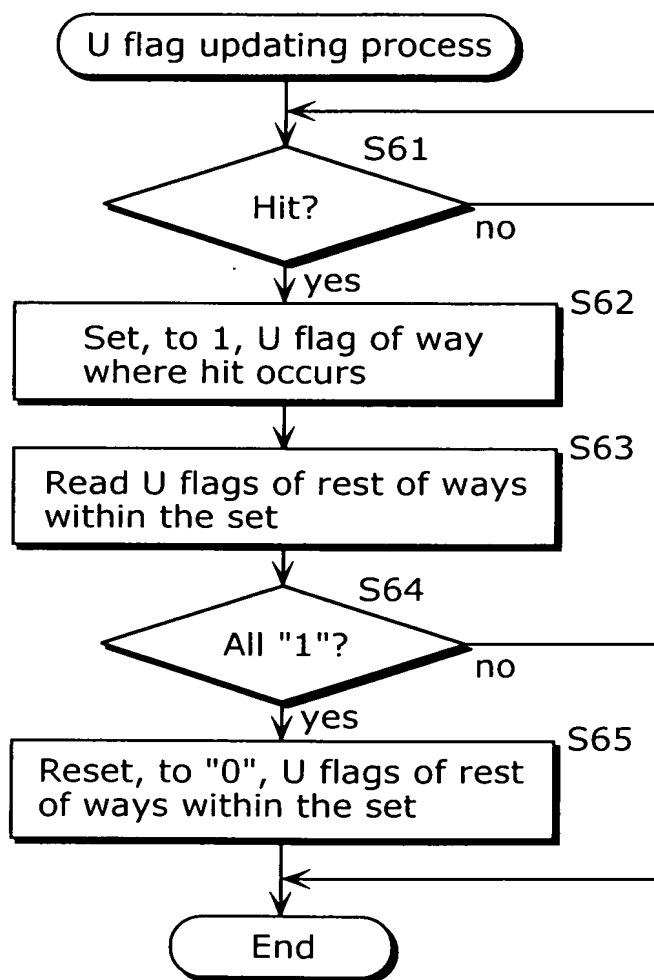


FIG. 18

